



NARAYANA ENGINEERING COLLEGE :: GUDUR

(Autonomous)

(Approved by AICTE, Affiliated to JNTUA & Accredited by NAAC with 'A')



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

EVENTS 2023-24

Value-Added Courses

S.No.	Name of the value added course	Year & Sem	Year of offering	Date (from-to)	No. of contact Hours	No. of students enrolled	Resource Person	In association with
1	IoT with NodeMCU	III B.Tech & I Sem	2023	06-11-2023 to 11-11-2023	48	175	Mr. R. Vikas Reddy and Team, Director, Technotran, Nellore	Technotran, Nellore.
2	PCB Design and Fabrication	II B.Tech & I Sem	2023	15-11-2023 to 21-11-2023	48	164	Mr. R. Vikas Reddy and Team, Director, Technotran, Nellore	Technotran, Nellore.
3	Tech for Society on Arduino (IOC)	II B.Tech & II Sem	2024	19-03-2024 to 22-03-2024	32	165	Mr. Ravikanth Kothuri, Team Lead, Capgemini Dr. Sripath Roy, General Secretary, Swecha AP, Mr. Santhu Vardhan, Developer, Cognizant.	Swecha, AP, Vijayawada

Faculty Development Program Organized

No.of Programs	FDP/PDP	Date	Name of theProgram	No. of Participants
1	FDP	13-17 May 2024	One week FDP on Antennas for 5G Communications & Beyond	45
2	FDP	22-27 January 2024	Applications of Artificial Intelligence in ECE	35

Circuit Club

S.No	Date	Titleof theEvent	Year/Class	Venue	No of Participant
1	07-11-2023	Technical Quiz	II ECE IIIECE	Respective Class Rooms	24
2	07-02-2024	Circuit Building	II ECE IIIECE	Project Lab	30

Bridge Courses

S.No.	Academic year-Sem	Year-Branch	Subject Name	Staff
1	2023-24 (I Sem)	IV ECE	Basic Concepts of VLSI Design	Mr.K.V.Bhanu Prakash
2			Introduction to field Programmable Gate Array	Mrs.M.Divya Vani
3	2023-24 (II Sem)	III ECE	Fundamentals of Digital Signal Processing	Mrs.Y.Neeraja
4		II ECE	Introduction to Electromagnetic Theory & Transmission Lines	Dr.B.Malakonda Reddy
5		III ECE	Basic Concepts of VLSI Design	Mr.S.Brahmaiah